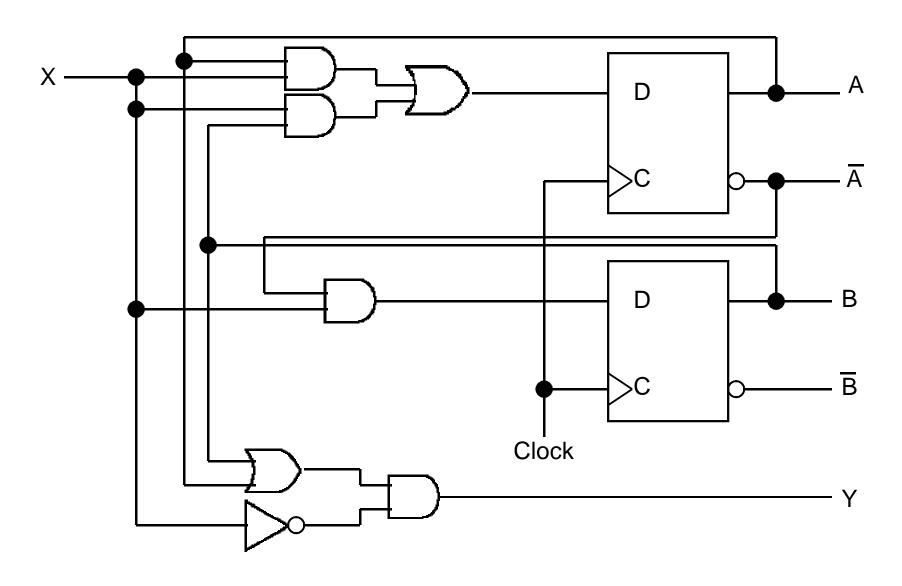
Analysis and Designing of Sequential Circuit

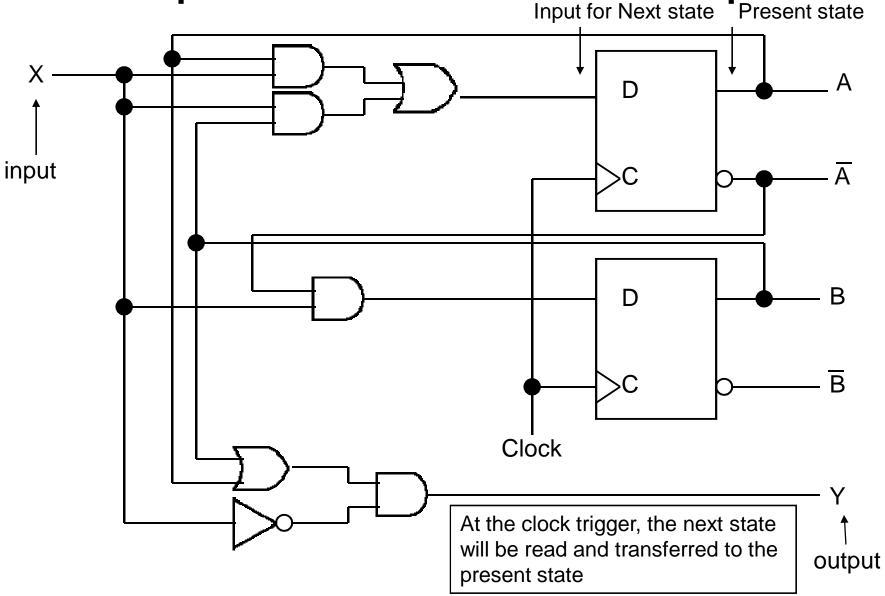
To analyze sequential circuits

- Find Boolean expressions for the outputs of the circuit and the flip-flop inputs.
- Use these expressions to fill in the output and flip-flop input columns in the state table.
- Finally, use the characteristic equation or characteristic table of the flip-flop to fill in the next state columns.
- The result of sequential circuit analysis is a state table or a state diagram describing the circuit.

Sequential Circuit Description



Sequential Circuit Description Input for Next state Present state



Input Equations

$$A_{next} = A_{present}X + B_{present}X$$

 $B_{next} = A'_{present}X$

$$B_{\text{next}} = A'_{\text{present}} X$$

$$Y = (A_{present} + B_{present})X'$$

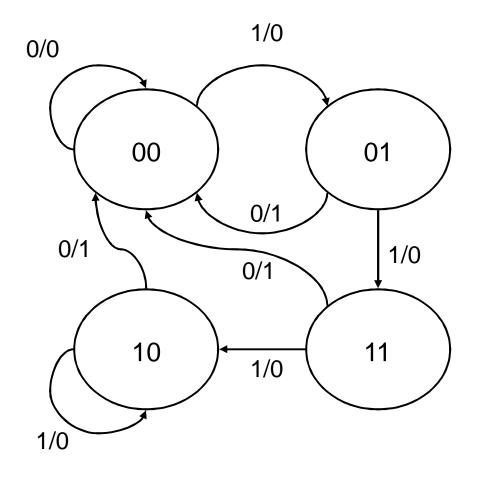
Next state in terms of input and present state

Output in terms of input and present state

State Table

Present State		Input	Next State		Output
Α	В	X	А	В	Υ
0	0	0	0	0	0
0	0	1	0	1	0
0	1	0	0	0	1
0	1	1	1	1	0
1	0	0	0	0	1
1	0	1	1	0	0
1	1	0	0	0	1
1	1	1	1	0	0

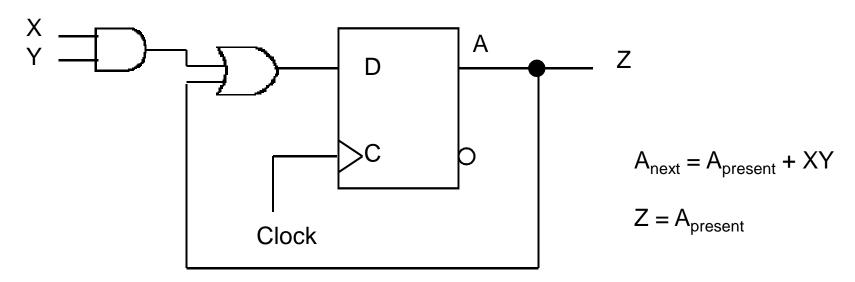
State Diagram



Mealy and Moore Models

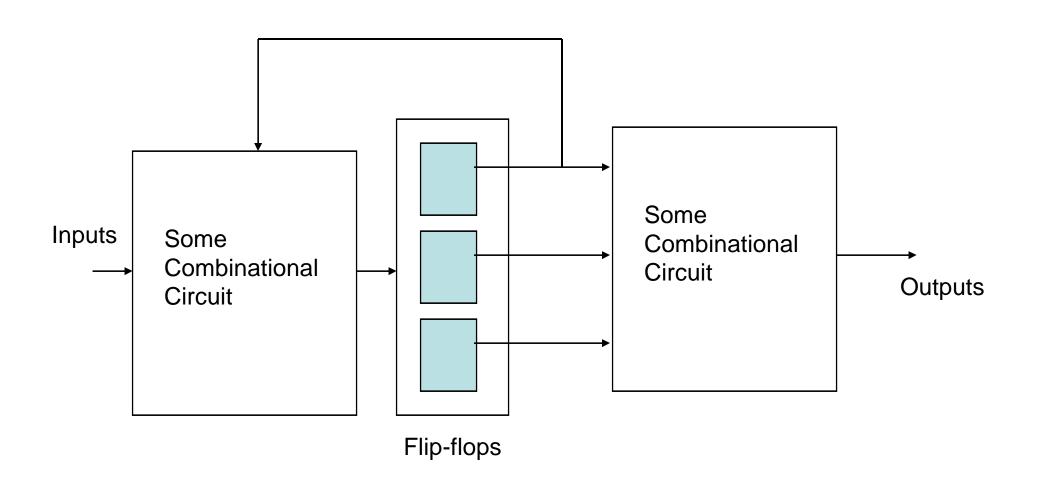
- Preceding Example: Output depends on present state and input. This is called the Mealy Model
- Another kind of circuit: Output only depends on present state. This is called the Moore Model

Example of Moore Model

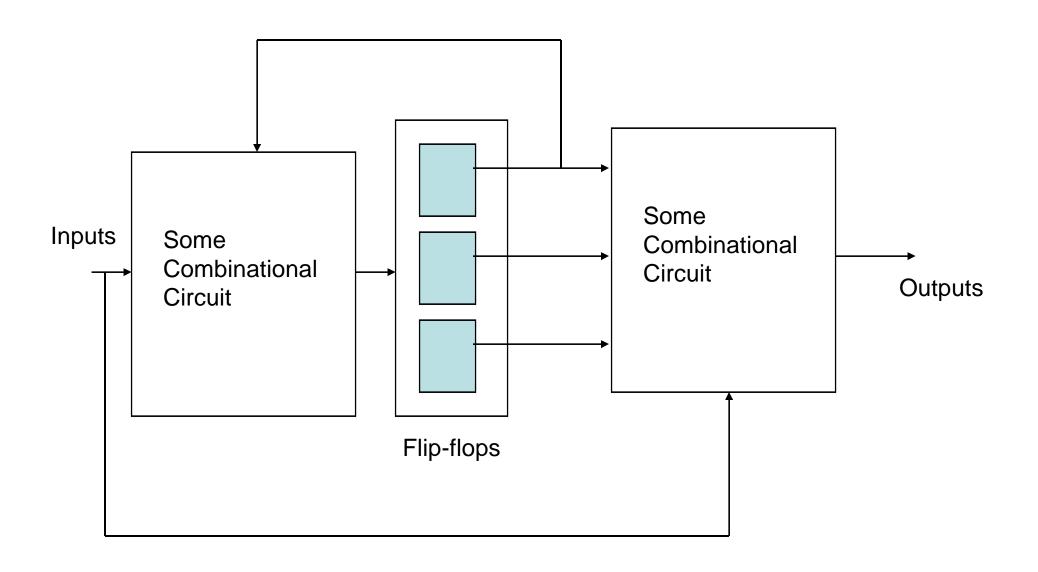


X	Y	A _{present}	A_{next}	
0	0	0	0	00.01.10
0	0	1	1	00,01,10
0	1	0	0	
0	1	1	1	$\sqrt{}$
1	0	0	0	$\left(\begin{array}{c}0/0\end{array}\right) \left(\begin{array}{c}1/1\end{array}\right)$
1	0	1	1	
1	1	0	1	
1	1	1	1	00,01,10,11

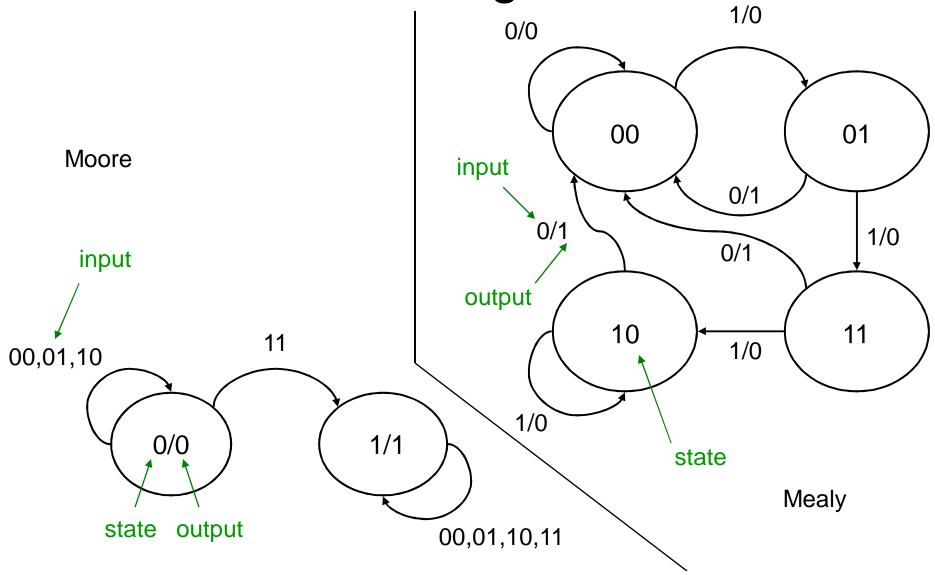
Moore Model



Mealy Model



Mealy and Moore Model State Diagrams

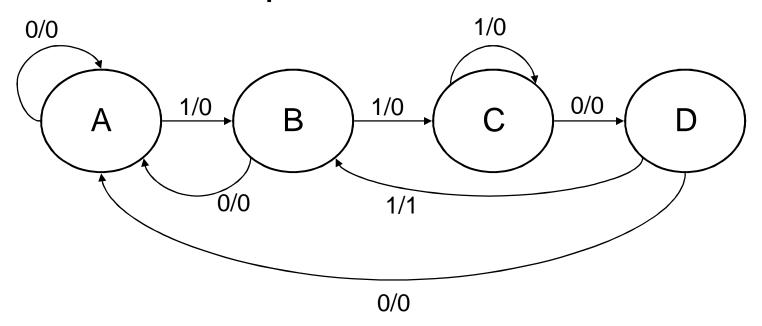


How to Design a Sequential Circuit

- 1. Specification
- 2. Formulation: Draw a state diagram
- 3. Assign state number for each state
- 4. Draw state table
- 5. Derive input equations
- 5. One D flip-flop for each state bit

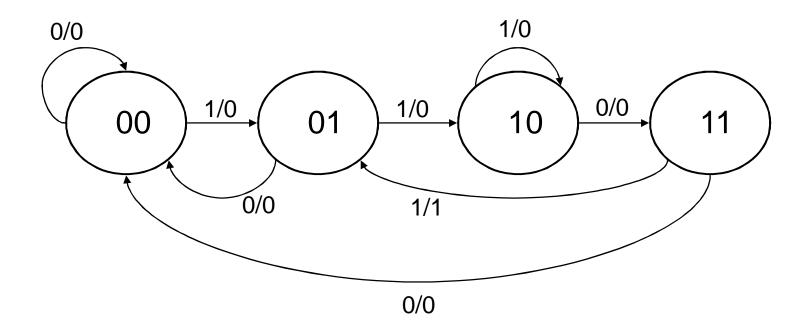
Example

- Design a sequential circuit to recognize the input sequence 1101.
- That is, output 1 if the sequence 1101 has been read, output 0 otherwise.



Assign States

4 states, so we need 2 bits



Draw State Table

Present State		Input	Next State		Output
Α	В	X	А	В	Υ
0	0	0	0	0	0
0	0	1	0	1	0
0	1	0	0	0	0
0	1	1	1	0	0
1	0	0	1	1	0
1	0	1	1	0	0
1	1	0	0	0	0
1	1	1	0	1	1

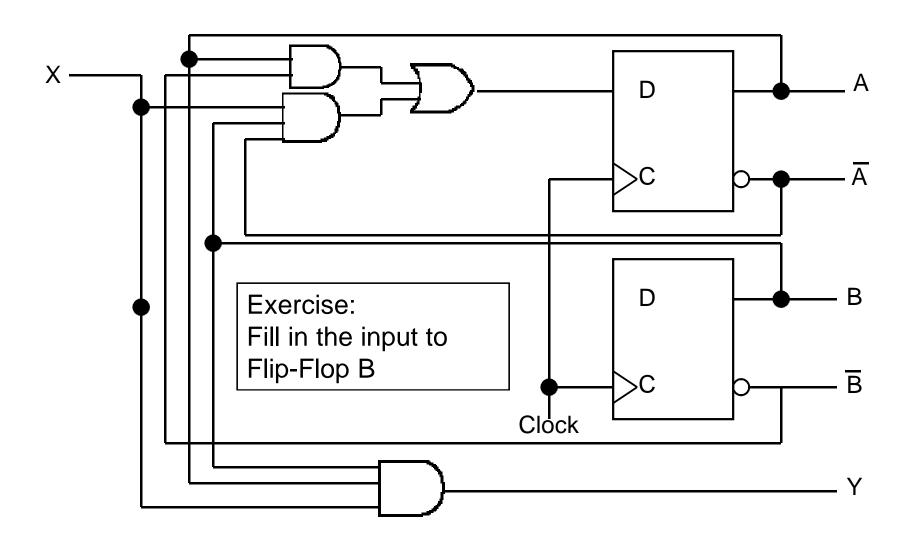
Derive Input Equations

$$A_{next} = A'BX + AB'$$

$$B_{\text{next}} = A'B'X + AB'X' + ABX$$

$$Y = ABX$$

Draw Circuit



Assignment

 Differentiate between Mealey and Moore Model?